

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Ramachandran KRISHNASWAMY et al.

Application No.: 10/772,434

Confirmation No.: 5995

Filed: February 6, 2004

Art Unit: 2128

For: METHOD AND STRUCTURE TO DEVELOP
A TEST PROGRAM FOR SEMICONDUCTOR
INTEGRATED CIRCUITS

Examiner: S. Lo

AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

This is in response to the Office Action dated January 22, 2009, for which a response was due on April 22, 2009. Filed herewith is a Petition and fee for a two-month extension of time, thereby extending the deadline for response to June 22, 2009. Accordingly, this response is timely filed. Reconsideration and allowance of the pending claims in light of the amendment and remarks presented herein are respectfully requested.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.